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DATE:

September 17, 2004

TO:

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Confirmation No.: 8820 Group Art Unit: 2816 Examiner: Cox, Cassandra F.

FROM:

Victor H. Okumoto

Reg. No. 35,973

PAGE 1 OF 21.

RE:

ELECTION/RESTRICTIONS AMENDMENT

Application Ser. No.: 10/621,055

"Clock Signal Processing Logic and Method for Determining Clock Signal Characteristics in Reference Voltage and Temperature Varying

Environments," Talledo, Cesar et al., filed 07/15/2003

(Atty. Docket IDTI.002US1-1826)

MESSAGE: Please find attached, a Non-Fee Amendment responding to the Notice of

Non-Compliant Amendment received for this case.

chr H. Olanh

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